

FIG. 1

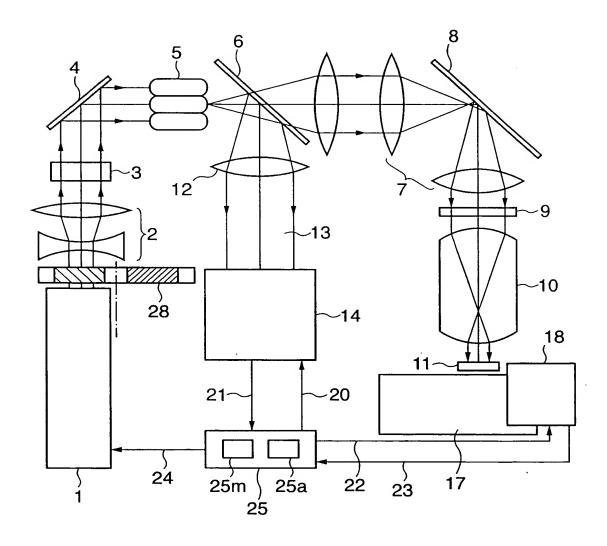
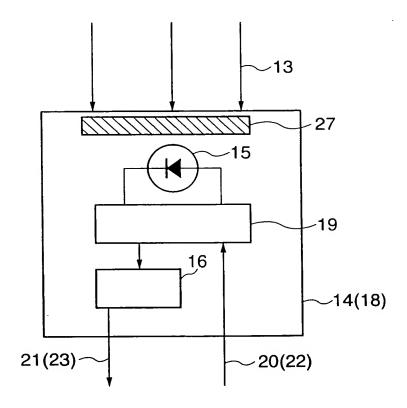
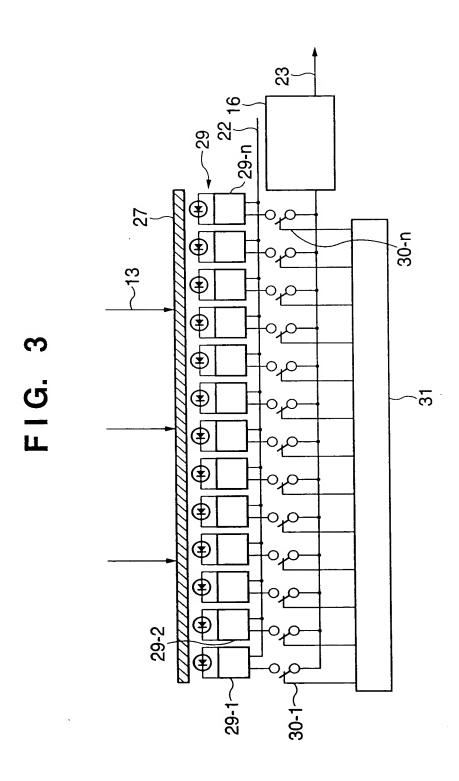




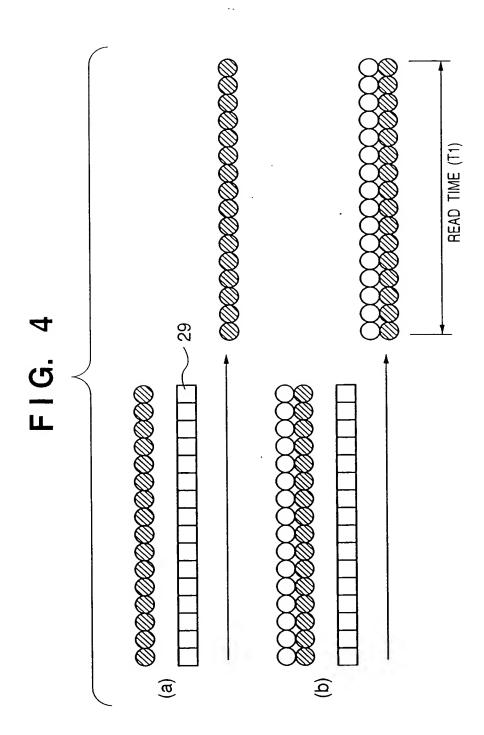
FIG. 2













## FIG. 5

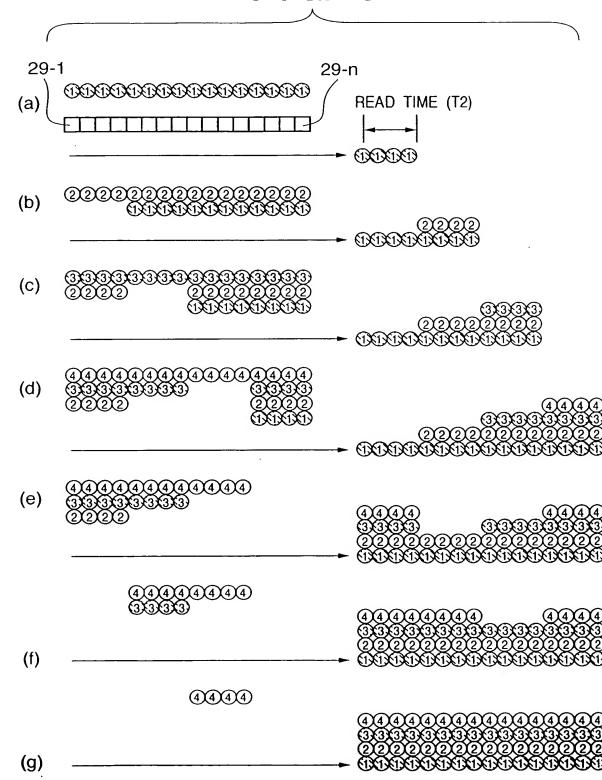
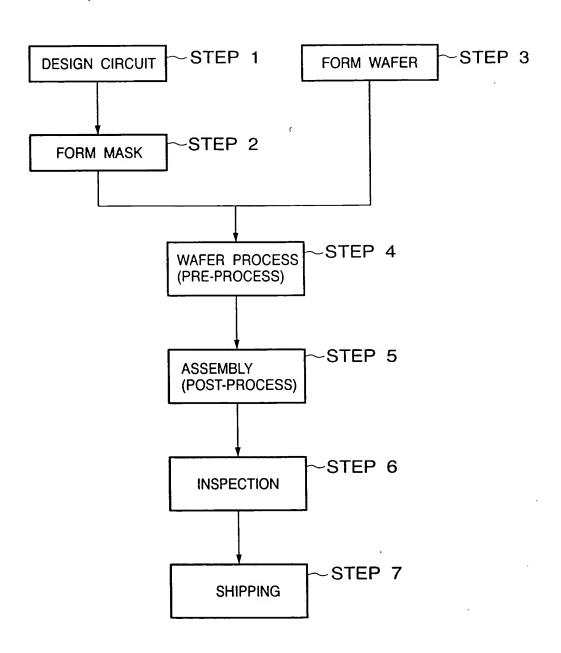




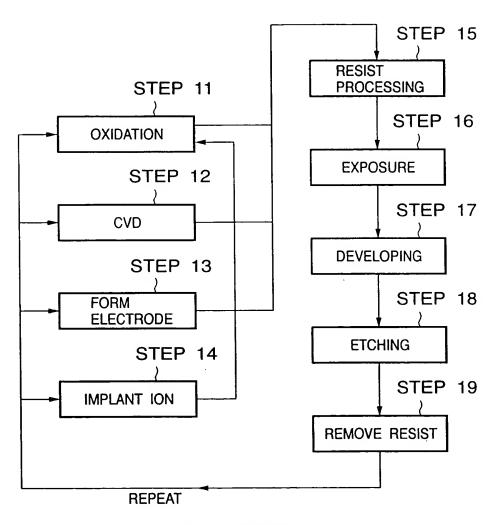
FIG. 6



SEMICONDUCTOR DEVICE MANUFACTURING FLOW



FIG. 7



WAFER PROCESS